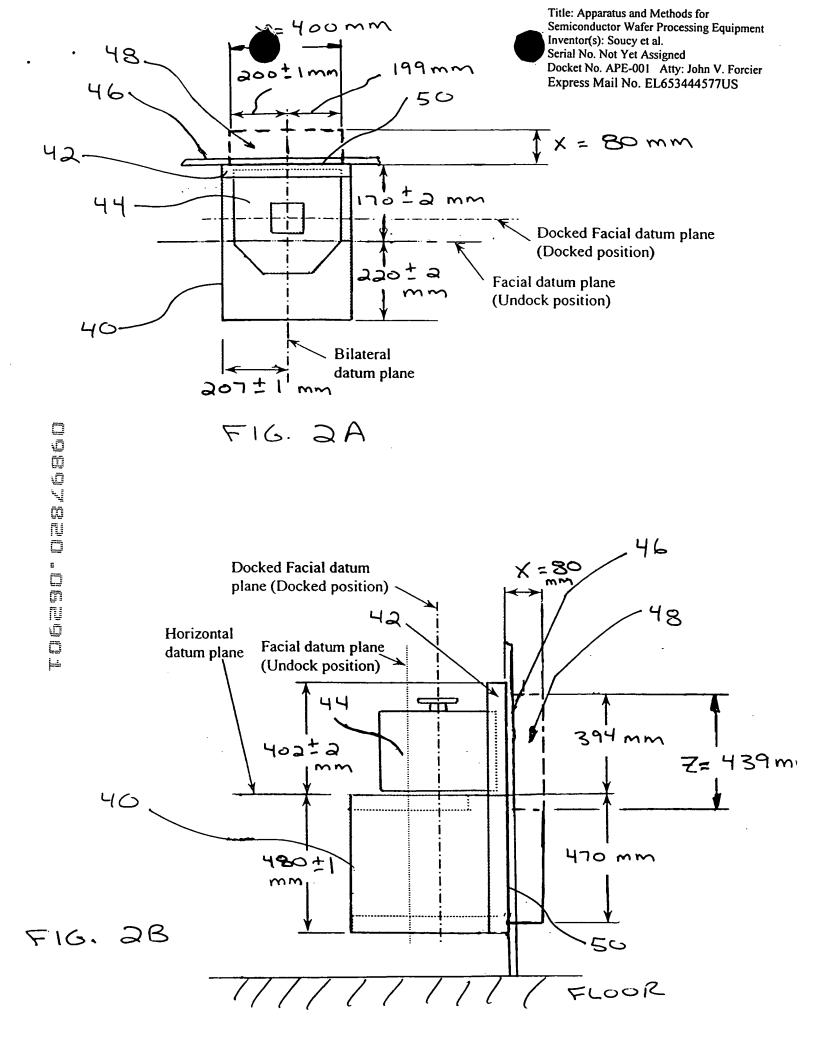
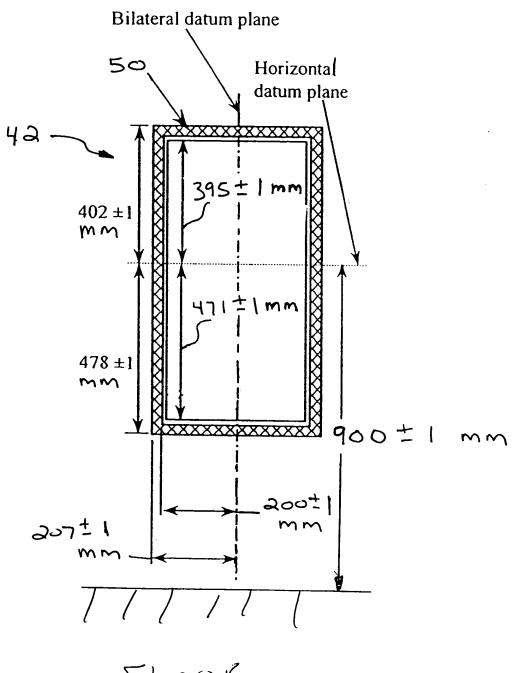


FIG. 1C (PRIOR ART)

FIG. 1D (PRIOR ART)



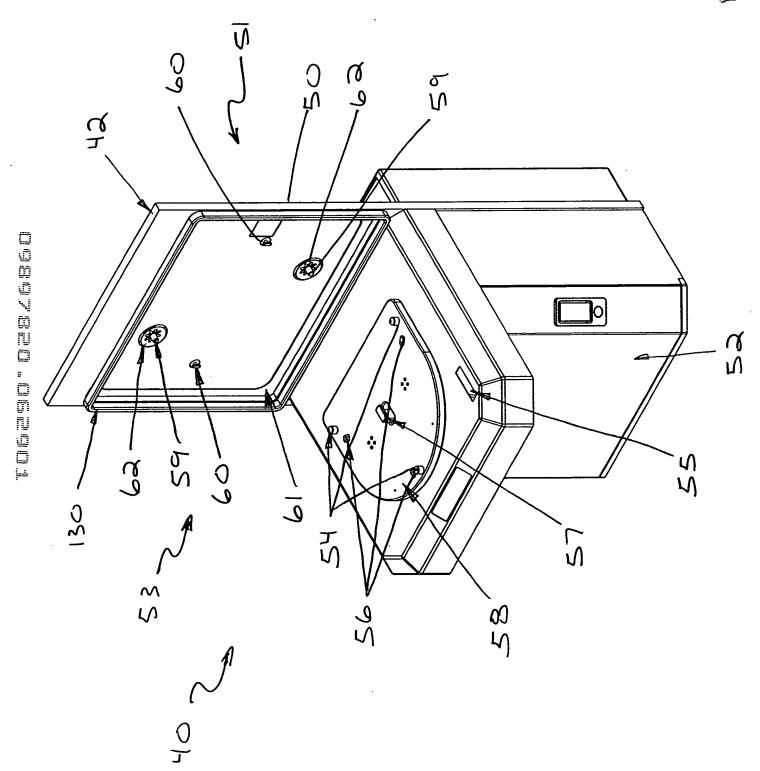




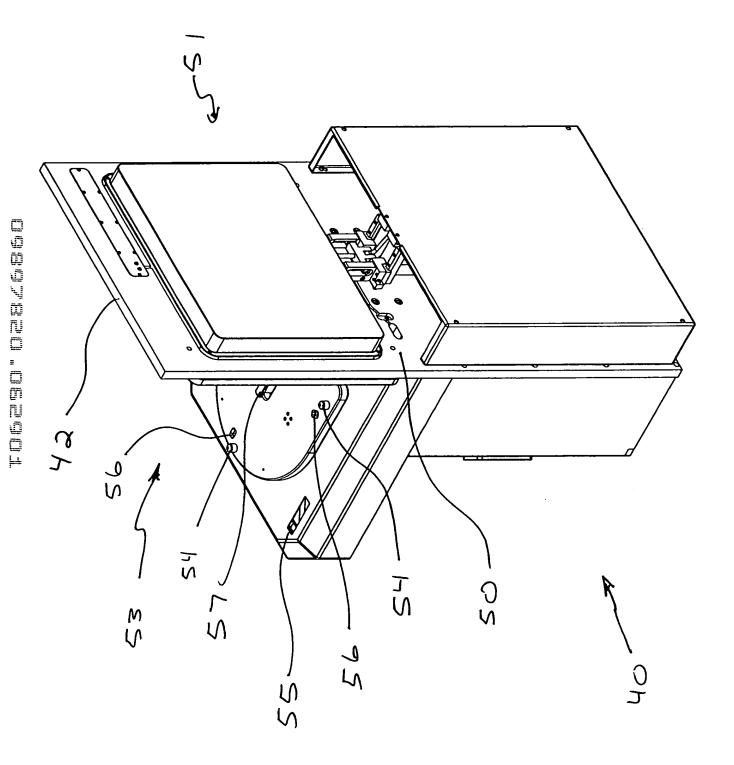
FLOOR

F16. 20

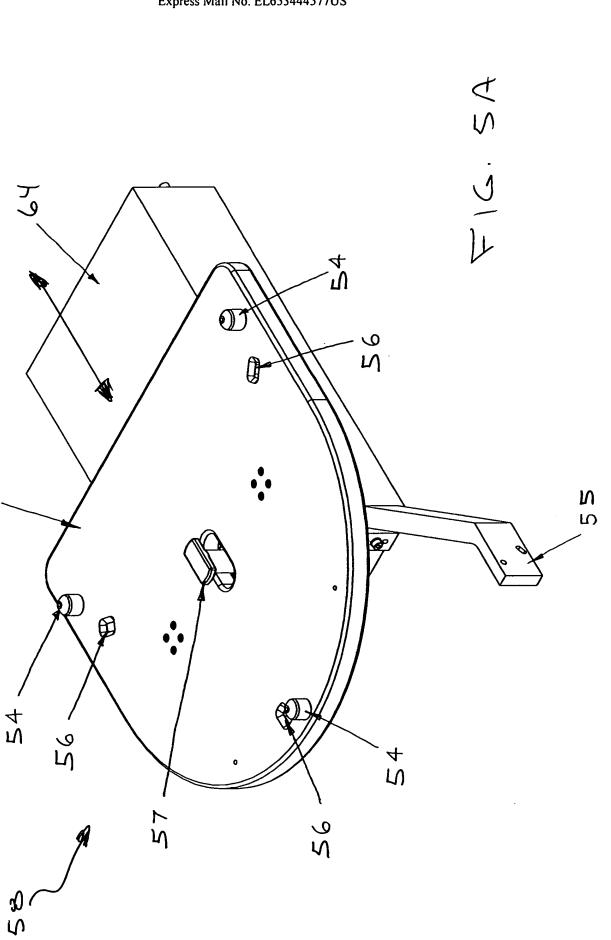
F. G. 3

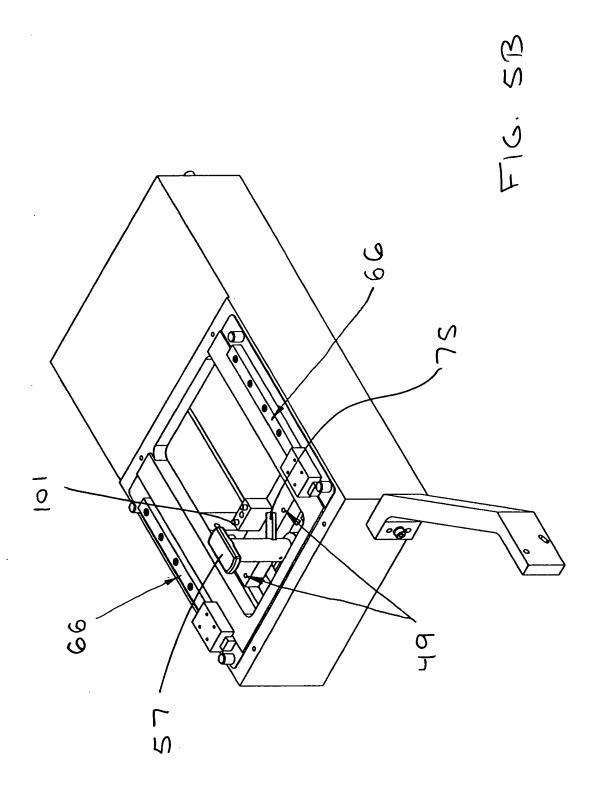


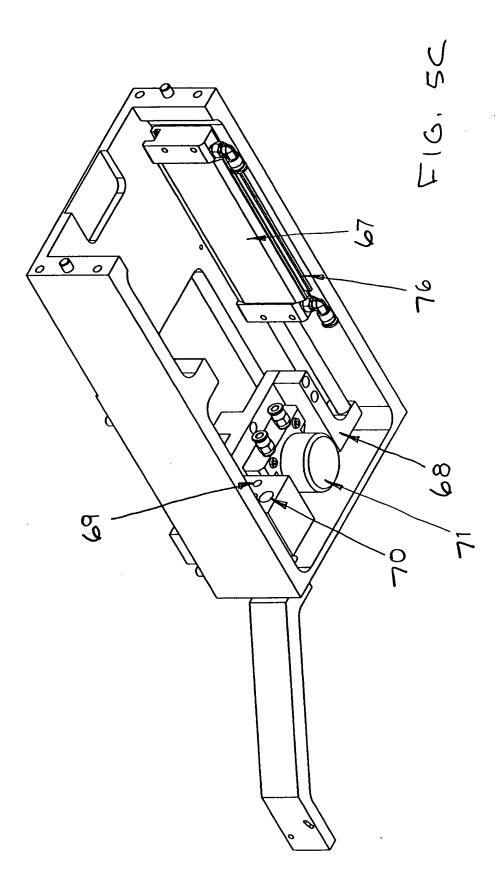
F1G. 4

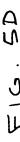


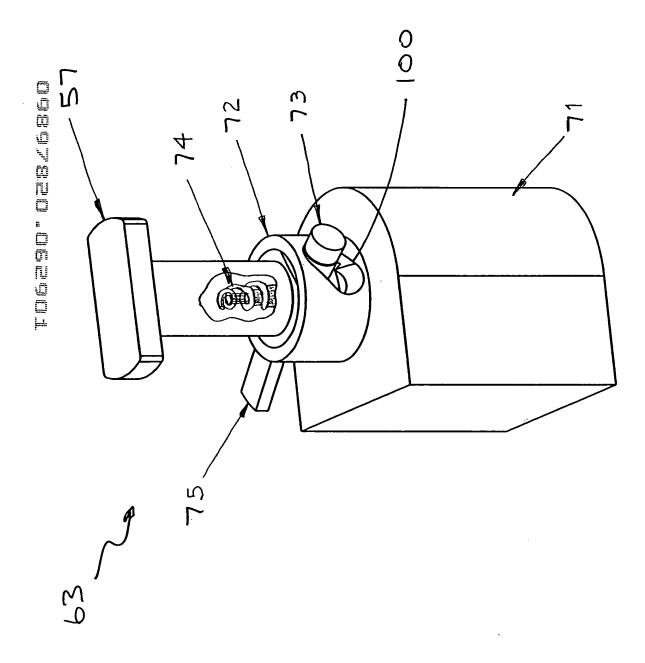
65





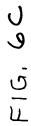


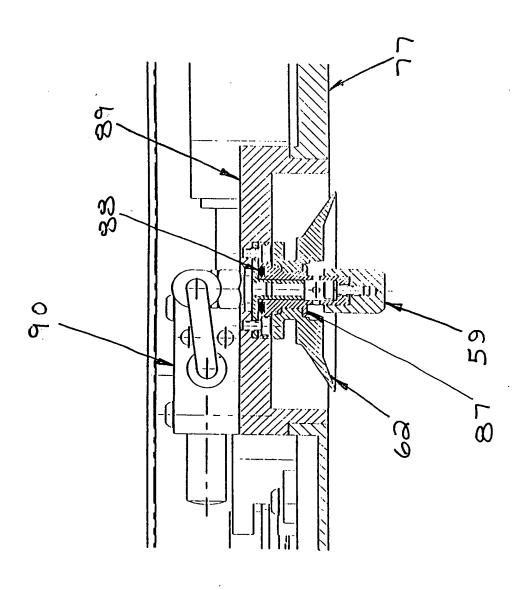


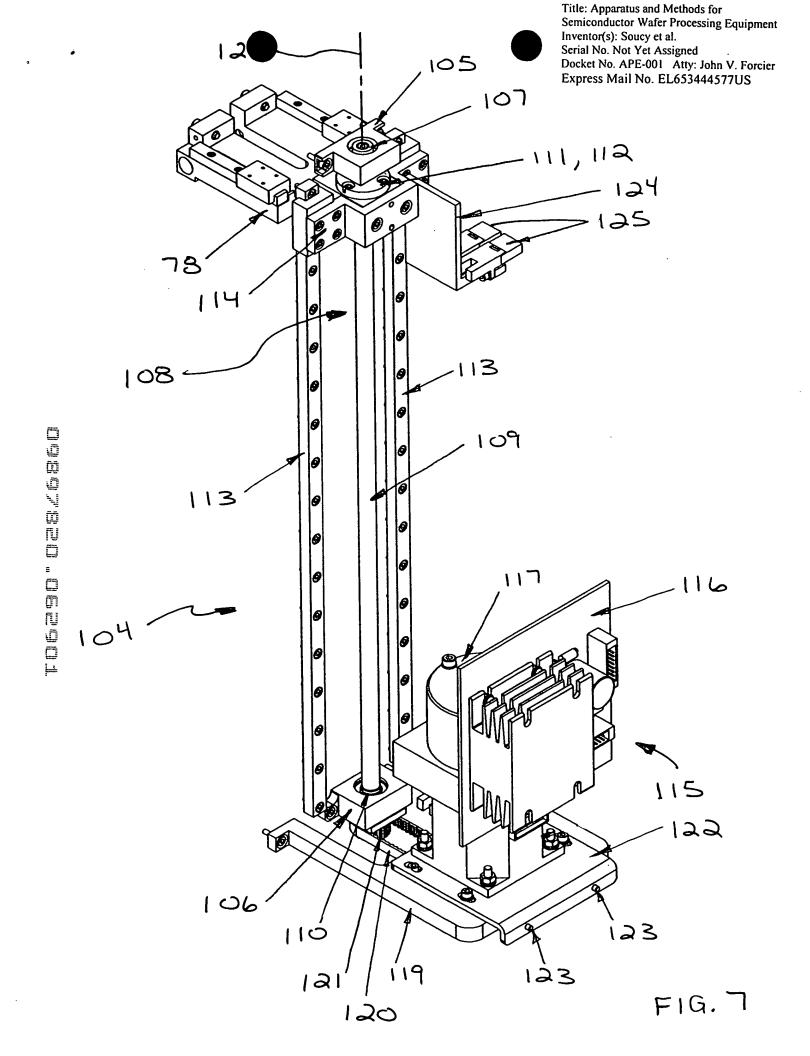


Title: Apparatus and Methods for Semiconductor Wafer Processing Equipment Inventor(s): Soucy et al.
Serial No. Not Yet Assigned
Docket No. APE-001 Atty: John V. Forcier Express Mail No. EL653444577US 6C 62 59 60 COGGYGEO.CGEGOX 60 60 62 <u>5</u>9 FIG. 6A

Title: Apparatus and Methods for Semiconductor Wafer Processing Equipment Inventor(ucy et al. Serial No. APE-001 Atty: John V. Forcier Express Mail No. EL653444577US 3 +06290" D28/6860 8 C FIG. bB







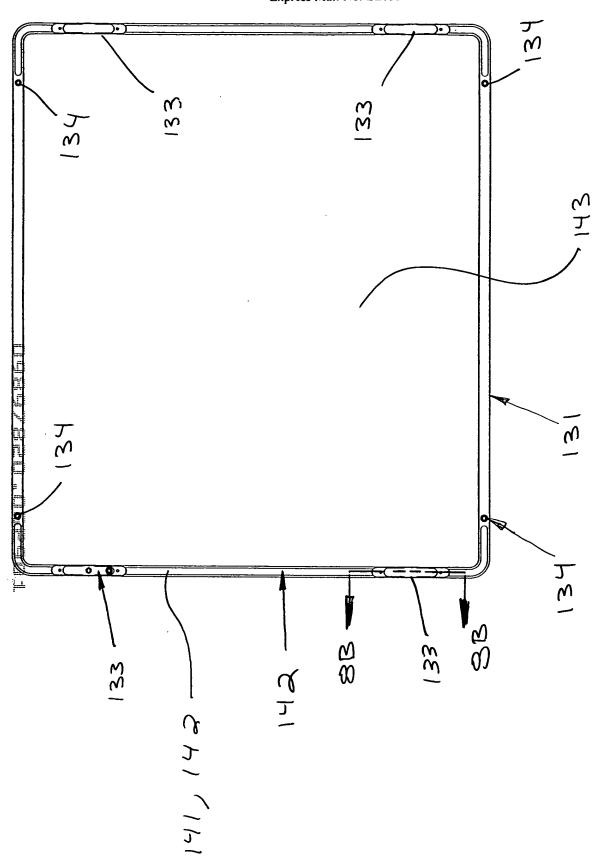
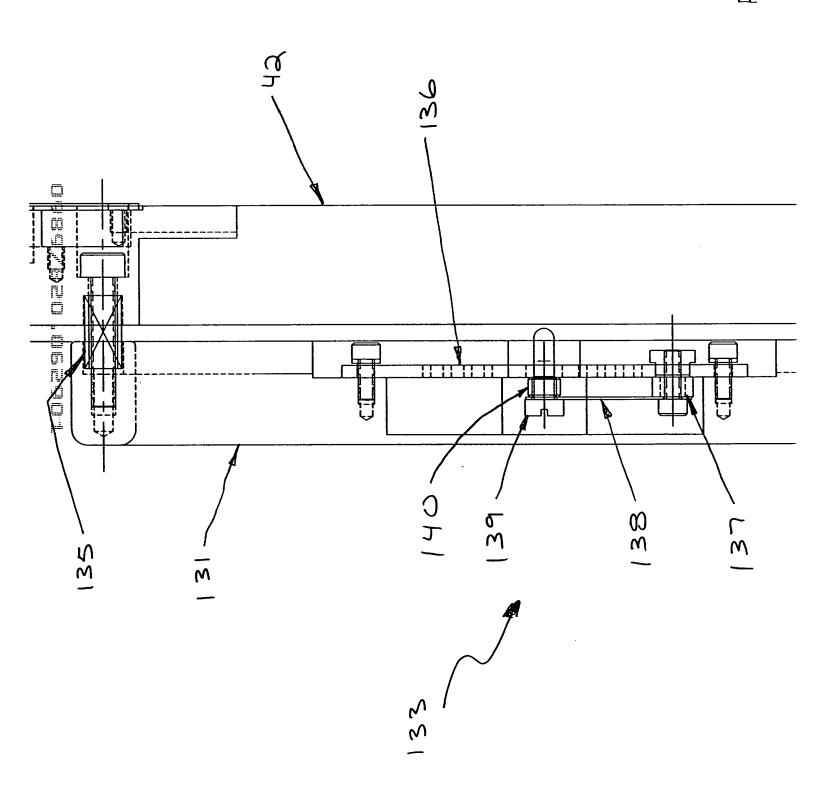
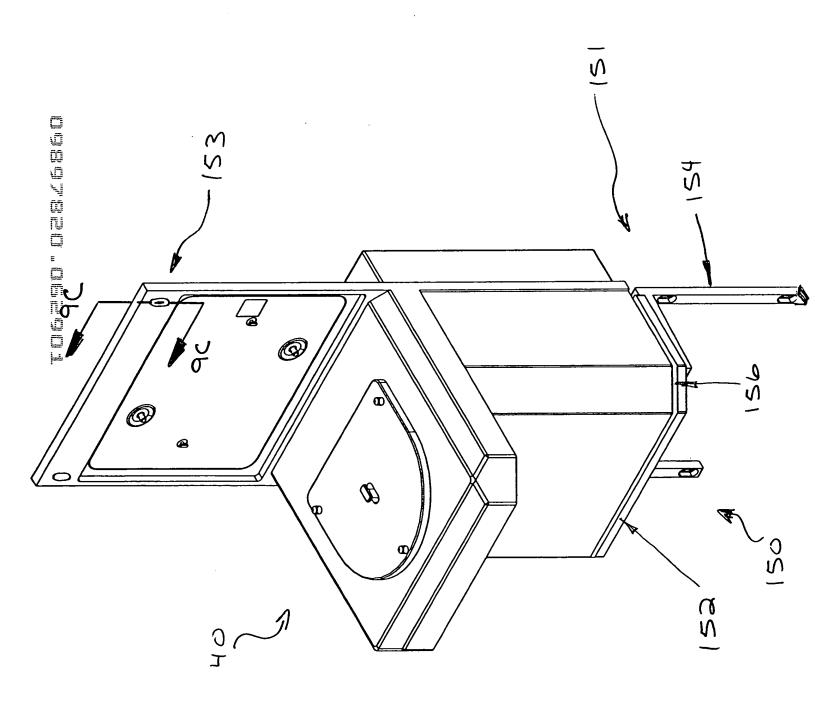


FIG. 3A

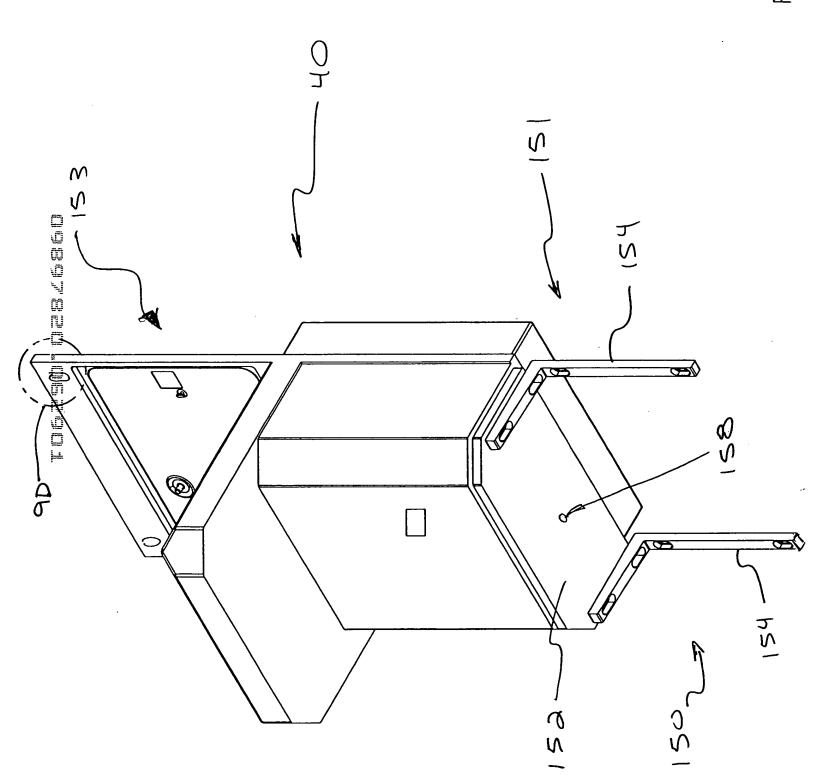
16.8R

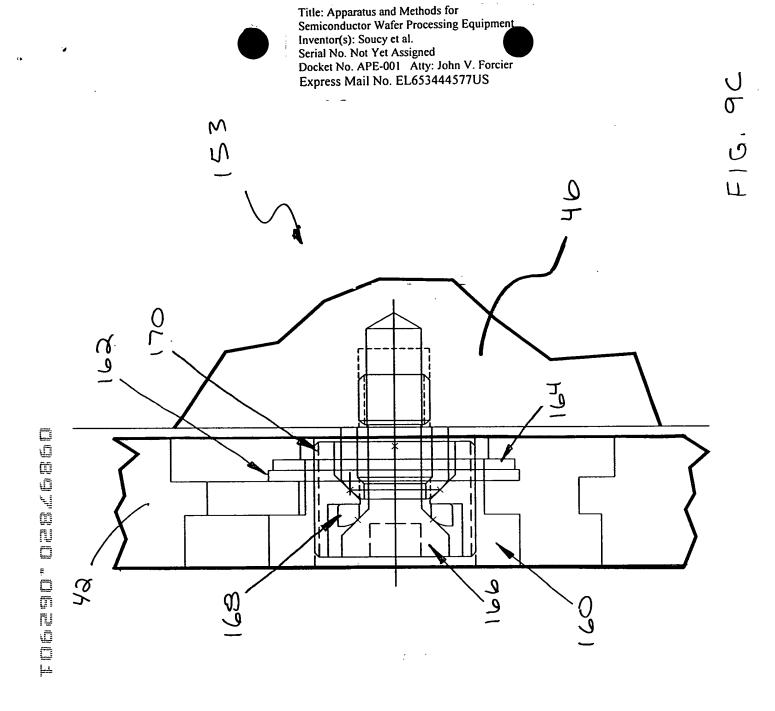


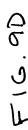
F1G, 9A

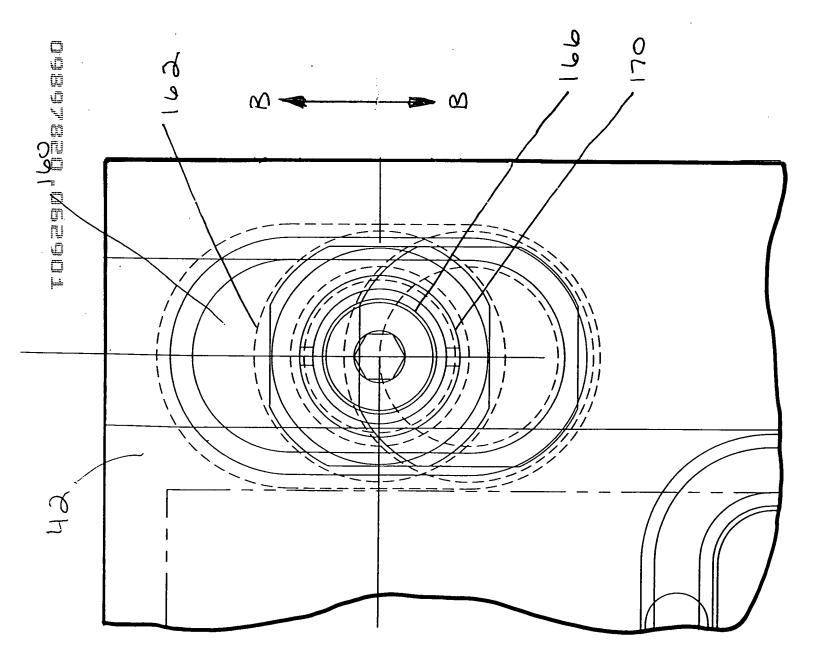


11G. 9B





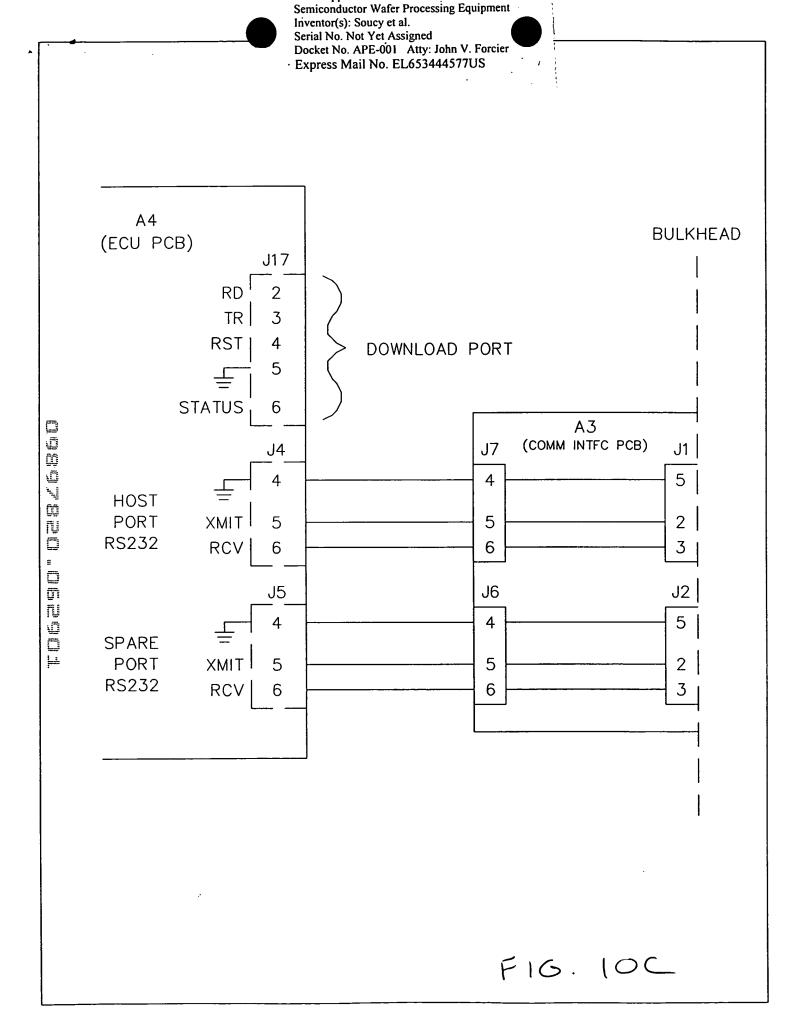




Title: Apparatus and Methods for

Semiconductor Wafer Processing Equipment

Title: Apparatus and Methods for Semiconductor Wafer Processing Equipment Inventor(s): Soucy et al. Serial No. Not Yet Assigned Docket No. APE-001 Atty: John V. Forcier Express Mail No. EL653444577US **BULKHEAD** A2 Sk (DC PWR SUPPLY) +24 J3 SK4 SK3 **A4** J1 Α1 (ECU PCB MODULE) (PWR ENTRY MODULE) L +24 5 AC 3 2 RTN RTN2 110 VAC 3 G 3 G 4 E1 FIG. 103



Title: Apparatus and Methods for

Semiconductor Wafer Processing Equipment ventor(s): Soucy et al. rial No. Not Yet Assigned Docket No. APE-001 . Atty: John V. Forcier Α5 Express Mail No. EL653444577US (PNEUMATIC INTERFACE) x = 24 VES PNEUMATIC DIST POD PLATE OUT -14--12-Α4 -12-POD PLATE IN 20 (ECU PCB) -14-PE3-6 -14-POD LATCH PE3-7 POD UNLATCH -12-PE6-0 ⇒ FIMS IN -14-PE6-1 -12-⇒ FIMS OUT PE6-2 KEY LATCH -12--12-PE6-3 ⇒ KEY UNLATCH -14--00-PE6-5 SPARE -14--M-> VACUUM ÷-__ MAIN AIR SOLENOID AIR OUT Ø B ΠŪ AIR SUPPLY IN Ê MAIN AIR SUPPLY SENSE AIR SUPPLY IN m 3 NC N.O. NC 2 ΓU VACUUM SENSE SWITCH WACUUM NC NC 2 N.O. BULKHEAD J13 A3 (COMM INTFC) ON BOARD E-STOP (EXTERNAL E-STOP) 6 3 7 24 VES (FIG. (OD)

Title: Apparatus and Methods for

